



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rajski et al.

Application No. 10/781,031

Filed: February 17, 2004

Confirmation No. 2828

For: METHOD FOR SYNTHESIZING LINEAR
FINITE STATE MACHINES

Examiner: David H. Malzahn

Art Unit: 2193

Attorney Reference No. 1011-67627-01

CERTIFICATE OF MAILING

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP RCE, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on the date shown below.

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INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)(4)

Listed on the accompanying form PTO-1449 and enclosed herewith is an English-language document and two Japanese-language patent references. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Japanese Patent Publication 05-215816 recites an information processing device.

Japanese Patent Publication No. 11-030646 recites a semiconductor integrated circuit and test circuit to be comprised therein.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S. applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

Applicants filed this Information Disclosure Statement ("IDS") before the mailing of a first Office action after the filing of a request for continued examination. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

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**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Attorney Docket Number	1011-67627-01
Application Number	10/781,031
Filing Date	February 17, 2004
First Named Inventor	Rajski
Art Unit	2193
Examiner Name	David H. Malzahn

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee
		Japan	05-215816 (w/ English abstract)	8.27.1993	Yasushi
		Japan	11-030646 (w/ English abstract)	2.2.1999	Hideki

OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS
		Lew Yan Voon et al., "BIST Linear Generator based on Complemented Outputs," <i>IEEE VLSI Test Symposium</i> , pp. 137-142 (1992).

EXAMINER SIGNATURE:	DATE CONSIDERED:
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* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.